



क्रमांक: Acad / Ph.D. Adm./Visvesvaraya Scheme/ 77 / 2025-26

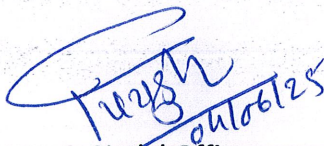
दिनांक: 04/06/2025

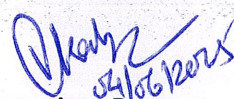
Ph. D. Admission under Visvesvaraya Scheme
Academic Year: - 2025-26

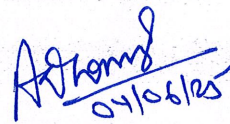
List of Eligible / Provisionally Eligible / Not Eligible Candidates

- The Following Candidates are requested to Report at Department of Electronics Engineering on **18/06/2025 at 10:00 AM** with **all Original Documents**. Short listed candidates must remain present for the interview/presentation at Room No. EC004N Conference Room, Ground Floor, DoECE.
- The interview will **start at 11:00 AM**. For the interview, candidates are required to give a presentation as per following guidelines.
1. The presentation should not be more than maximum 10 minutes' duration.
 2. Power point presentation is must.
 3. Maximum number of slides including title slide should not be more than 8.
 4. The presentation should contain background of topic, literature survey, research gap, problem identification and research methodology.
- Please contact Prof. Piyush N. Patel (M. 8200301738) for any further information.

Sr. No.	Name of the Candidate	Category	Ph.D. Cat.	Name of the Department in which the Ph.D. application is Submitted	Eligible / Provisionally Eligible / Not Eligible	Remark
1.	Chinni Krishnaiah G	SC	Full Time	DoAI	Eligible	-
2.	Patel Roshni Arvindbhai	OBC-NCL	Full Time	DoAI	Not Eligible	Non Gate
3.	Chinni Krishnaiah G	SC	Full Time	DoCSE	Eligible	-
4.	Ashish Kumar Mourya	Open	Part Time	DoCSE	Not Eligible	Not Fulfilling the Requirement of UG Degree
5.	Anshuman Singh	Open	Full Time	DoECE	Eligible	-
6.	Rajender Racherla	SC	Part Time	DoECE	Provisionally Eligible	Recent Exp. Certificate / Appointment Letter to be Submitted. NOC to be Submitted.
7.	Avinash Singh Chauhan	Open	Part Time	DoECE	Provisionally Eligible	NOC to be Submitted.


Nodal Officer
Visvesvaraya Ph.D Scheme


Associate Dean
(Academic – Ph.D)


Dean (Academic)